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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 4116  
Examiner: Robert T.W. OWEN

In Re PATENT APPLICATION of:

Applicant(s): Kazumasa TANIDA et. al. )  
U.S. Serial No.: 10/594,561 )  
U.S. Filing Date: September 27, 2006 )  
International Serial No.: PCT/JP05/013355 )  
International Filing Date: July 21, 2005 )  
Priority Date Claimed: September 28, 2004 )  
For: SEMICONDUCTOR DEVICE )  
WITH A SEMICONDUCTOR )  
CHIP CONNECTED IN A FLIP )  
CHIP MANNER (amended) )  
Attorney Reference: AI-427NP )

**AMENDMENT**

February 27, 2009

**Mail Stop Amendment**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Examiner's Action mailed on December 8, 2008, please  
amend the above-identified application as follows: